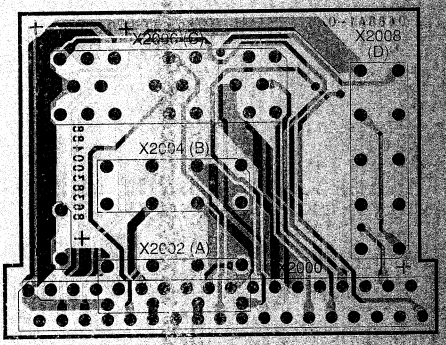
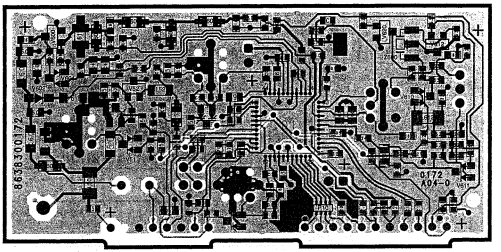
Schaltbild • Circuit diagram

Anschlußplatte
Connector board
PL 0488 A01

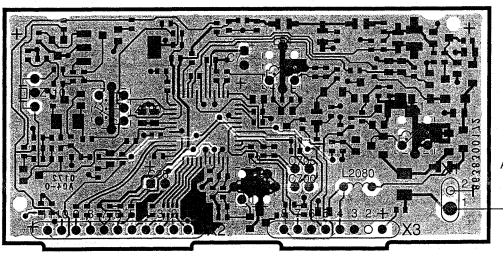


zu Schaltbild Audi Concert Plus A4/A6 und A8 8 622 401 015

HF-Platte
RF board
PL 0172 A04
Chip

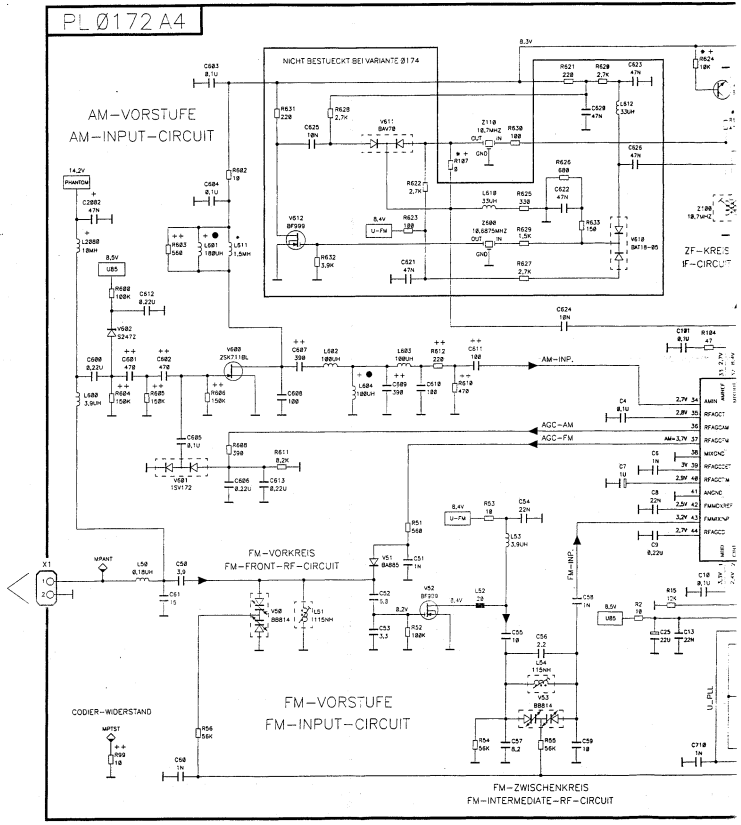


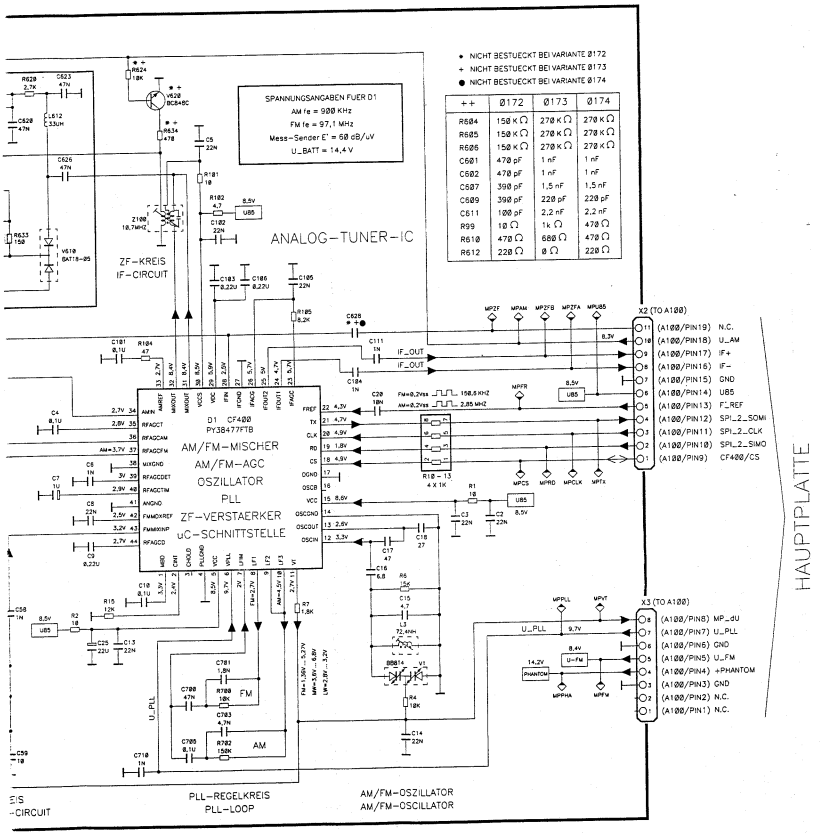
HF-Platte
RF board
PL 0172 A04



X2 (TO A100)
1 = CF400 / CS
2 = SPI_2_SIMO
3 = SPI_2_CLK
4 = SPI_2_SOMI
5 = F_REF
6 = U85 (8,5V)
7 = GND
8 = IF–
9 = IF+
10 = U_AM (8,3V)
11 = N.C.

X3 (TO A100)
1 = MP_ANT
2 = N.C.
3 = GND
4 = +PHANTOM (14V)
5 = U_FM (8,4V)
6 = GND
7 = U_PLL (9,7V)
8 = MP_dU


PL Ø172 A4
NICHT BESTUECKT BEI VARIANTE Ø174
AM-VORSTUFE
AM-INPUT-CIRCUIT
FM-VORKREIS
FM-FRONT-RF-CIRCUIT
FM-VORSTUFE
FM-INPUT-CIRCUIT
FM-ZWISCHENKREIS
FM-INTERMEDIATE-RF-CIRCUIT
ZF-KREIS
IF-CIRCUIT
CODIER-WIDERSTAND
ANTENNE
ANTENNA
AM-INP.
AGC-AM
AGC-FM
FM-INP.
U-PLL
8,4V
8,5V
14,2V
PHANTOM
U-FM
MPANT
MPST
X1
U85
V53
BB814
V50
BB814
V51
BA585
V52
BF909
V611
BA778
V612
BF999
V680
ZSA718L
V681
1SV172
V682
S2472
V610
BAT18-05
Z110
10,7MHz
Z680
10,6875MHz
Z100
10,7MHz
L50
0,18UH
L51
1,15NH
L52
28
L53
3,9UH
L54
115NH
L680
3,9UH
L681
180UH
L682
180UH
L683
180UH
L684
180UH
L610
33UH
L611
1,5MH
L612
33UH
C50
3,9
C51
1N
C52
5,6
C53
3,3
C54
22N
C55
18
C56
2,2
C57
8,2
C58
1N
C59
18
C60
1N
C61
15
C680
8,2K
C681
470
C682
470
C683
8,1U
C684
8,1U
C685
8,1U
C686
8,22U
C687
390
C688
100
C689
390
C610
180
C611
100
C612
8,22U
C613
8,22U
C621
47N
C622
47N
C623
47N
C624
18N
C625
18N
C626
C627
2,7K
C628
2,7K
C629
47N
C630
180
R50
18
R51
560
R52
180K
R53
18
R54
56K
R55
56K
R56
56K
R57
18
R99
18
R600
100K
R601
150K
R602
19
R603
560
R604
150K
R605
150K
R606
150K
R607
330
R608
330
R609
8,2K
R610
470
R611
8,2K
R612
220
R621
220
R622
2,7K
R625
330
R626
680
R627
2,7K
R629
1,5K
R630
180
R631
220
R632
3,9K
R633
180
R680
100K
R681
100K
R682
19
R683
560
R684
150K
R685
150K
R686
150K
R687
330
R688
330
R689
390
R690
180
R691
470
R2
18
R15
C2
47N
C3
8,22U
C4
8,1U
C5
1N
C6
1N
C7
1U
C8
22N
C9
8,22U
C10
8,1U
C11
47N
C12
8,22U
C13
22N
C25
22U
C680
100
C2680
47N
C2682
47N
C2688
18NH
C710
1N
C1191
8,1U
R624
10K
R1191
47
R184
47
AM-INP.
AGC-AM
AGC-FM
FM-INP.
U-PLL
AMINP
RFAGCT
RFAGCAM
RFAGCFM
MIXCNC
RFAGCSET
RFAGCTM
ANCNC
FMMIXREF
FMMIXINP
RFAGCC
AMIFO
2,7V 34
2,8V 35
36
AM=3,7V 37
38
2V 39
2,8V 40
41
2,5V 42
3,2V 43
2,7V 44
3,3V
2,4V

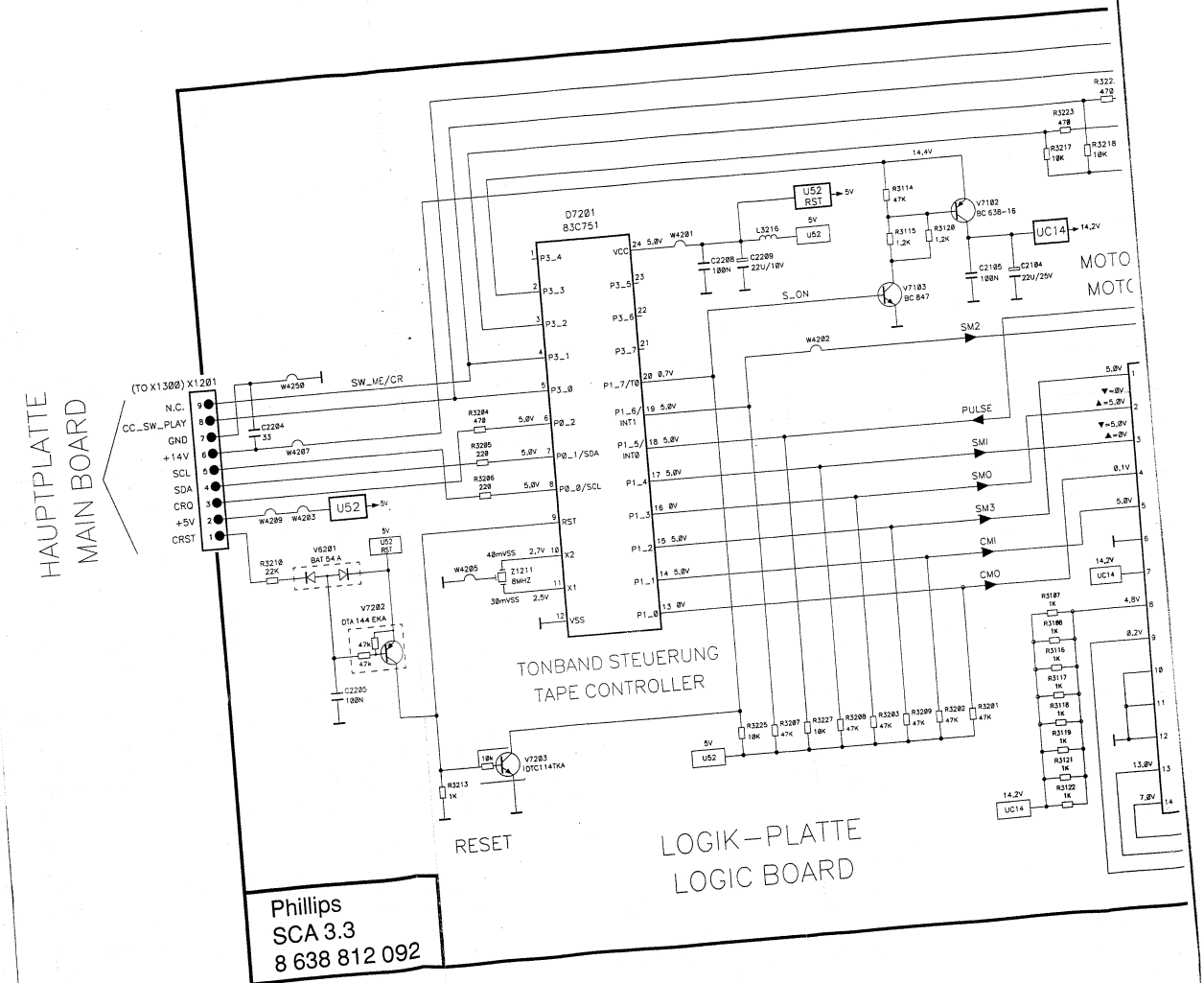


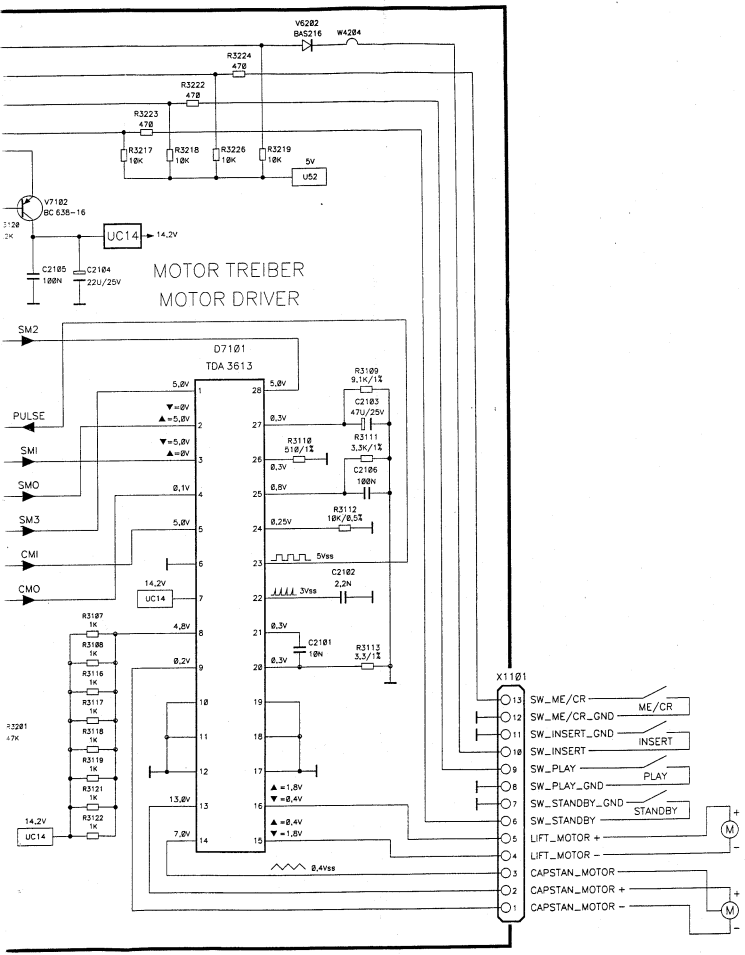
Prüfdiagnose Tuner IC (D1)
Diagnosis test tuner IC (D1)

Pin	Band	Frequenz	E'	Uss	Vermerke	Notice
24+25 (ZF-OUT)	FM	97,1 MHz	83 dbµV	650 mVss	jeweils gegen Masse	respective against GND
28	FM	97,1 MHz	80 dbµV	25 mVss		
31+32	FM	97,1 MHz	80 dbµV	200 mVss	jeweils gegen Masse	respective against GND
31+32	AM	900 kHz	80 dbµV	200 mVss	jeweils gegen Masse	respective against GND
34 (AM-IN)	AM	900 kHz	80 dbµV	50 mVss		
36	AM	900 kHz	ab 73 dbµV		künstliche Antenne aus	not commutated
37	FM	97,1 MHz	ab 80 dbµV			
43 (FM-IN)	FM	97,1 MHz	94 dbµV	5 mVss		

Pin-Belegung des FM/AM Tuner-IC D1
Tuner IC D1 Pin configuration

Pin No.	I/O	Name	Funktion	Function
1	-	MIXDEC	Mischer Entkopplung	Mixer decoupling
2	-	CINT	für PLL	for PLL
3	-	CHOLD	für PLL	for PLL
4	-	PLLGND	PLL - Masse	PLL Ground
5	-	VCC	8,5V	8,5V
6	-	VPLL	PLL Oberspannung	PLL top voltage
7	I	LFINP	Schleifenfiltereingang	PLL loop filter Input
8	O	LF1	Schleifenfilter 1	PLL loop filter Output 1
9	O	LF2	Schleifenfilter 2	PLL loop filter Output 2
10	O	LF3	Schleifenfilter 3	PLL loop filter Output 3
11	I	VTUNE	Abstimmspannung	Tuning voltage
12	I	OSCINP	Oszillator Eingang	Oscillator Input
13	O	OSCOUT	Oszillator Ausgang	Oscillator Output
14	-	OSCGND	Oszillator Masse	Oscillator Ground
15	O	VCC	8,5V	8,5V
16	O	OSCBUF	Oszillatorausgangstreiber	Oscillator Buffer Output
17	I	DGND	Digitale Masse	Digital Ground
18	I	CS	Chip Select	Chip Select
19	I	RD	Dateneingang	DATA IN
20	I	CLK	Clock	Clock
21	O	TX	Datenausgang	DATA OUT
22	I	FREF	Referenzfrequenz	Reference frequency
23	-	IFAGC2	ZF Regelspannung 2	IF AGC 2
24	O	IFOUT1	ZF - Ausgang 1	IF output 1
25	O	IFOUT2	ZF - Ausgang 2	IF output 2
26	-	IFAGC1	ZF Regelspannung 1	IF AGC 1
27	-	IFGND	ZF Masse	IF Ground
28	I	IFIN	ZF Eingang	IF Input
29	-	VDC	Interne Referenzspannung	Internal reference voltage
30	-	VCC	8,5V	8,5V
31	O	MIXOUT2	Mischerausgang 2	Mixer Output 2
32	O	MIXOUT1	Mischerausgang 1	Mixer Output 1
33	-	AMREF	AM - Referenzeingang	AM reference Input
34	I	AMMIXIN	AM Mischereingang	AM Mixer Input
35	-	RFAGC3	HF Regelzeitkonstante (aufregeln)	RF AGC 3
36	O	RFAGCAM	HF Steuerspannung Vorstufe AM	RF AGC for AM input stage
37	O	RFAGCFM	HF Steuerspannung Vorstufe FM	RF AGC for FM input stage
38	-	MIXGND	Mischer Masse	Mixer Ground
39	-	RFAGC2	HF Regelzeitkonstante (Detektor)	RF AGC 2
40	-	RFAGC1	HF Regelzeitkonstante (abregeln)	RF AGC 1
41	-	ANGGND	Analog Masse	Analog ground
42	-	FMMIXREF	Referenzspannung FM Mischer	Reference voltage FM mixer
43	I	FMMIXINP	FM Mischer Eingang	FM mixer input
44	-	RFAGCD	AGC Entkopplung	AGC decoupling

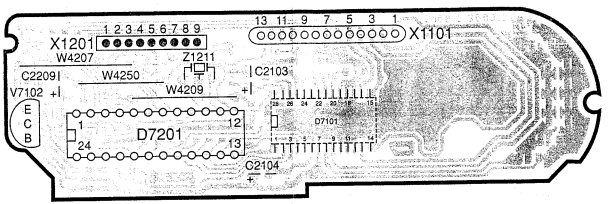

HAUPTPLATTE
MAIN BOARD
(TO X1300) X1201
N.C.
CC_SW_PLAY
GND
+14V
SCL
SDA
CRO
+5V
CRST
SW_ME/CR
W4250
C2204
33
W4207
W4209
W4203
U52
5V
D7201
83C751
P3_4
P3_3
P3_2
P3_1
P3_0
P0_2
P0_1/SDA
P0_0/SCL
RST
X2
X1
VSS
VCC
P3_5
P3_6
P3_7
P1_7/T0
P1_6/INT1
P1_5/INT0
P1_4
P1_3
P1_2
P1_1
P1_0
W4201
L3216
C2208
100N
C2209
22U/10V
U52 RST
R3114
47K
V7102
BC 638-16
R3115
1,2K
R3120
1,2K
V7103
BC 847
S_ON
UC14
14,2V
C2105
100N
C2104
22U/25V
MOTO
MOTC
W4202
SM2
PULSE
SMI
SMO
SM3
CMI
CMO
R3204
470
R3205
220
R3206
220
R3210
22K
V6201
BAT 54 A
V7202
DTA 144 EKA
C2205
100N
W4205
Z1211
8MHZ
40mVSS
2,7V
30mVSS
2,5V
TONBAND STEUERUNG
TAPE CONTROLLER
R3225
10K
R3207
47K
R3227
10K
R3208
47K
R3203
47K
R3209
47K
R3202
47K
R3201
47K
V7203
IDTC114TKA
R3213
1K
RESET
LOGIK–PLATTE
LOGIC BOARD
R3223
470
R3217
10K
R3218
10K
R3187
1K
R3188
1K
R3116
1K
R3117
1K
R3118
1K
R3119
1K
R3121
1K
R3122
1K
Phillips
SCA 3.3
8 638 812 092



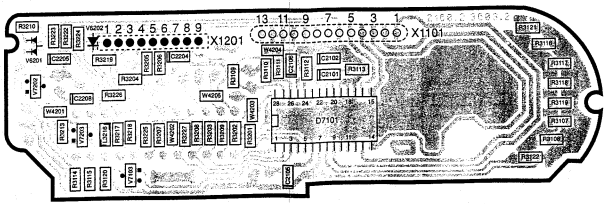
Logik-Platte
Logic board
(SCA 3.3)

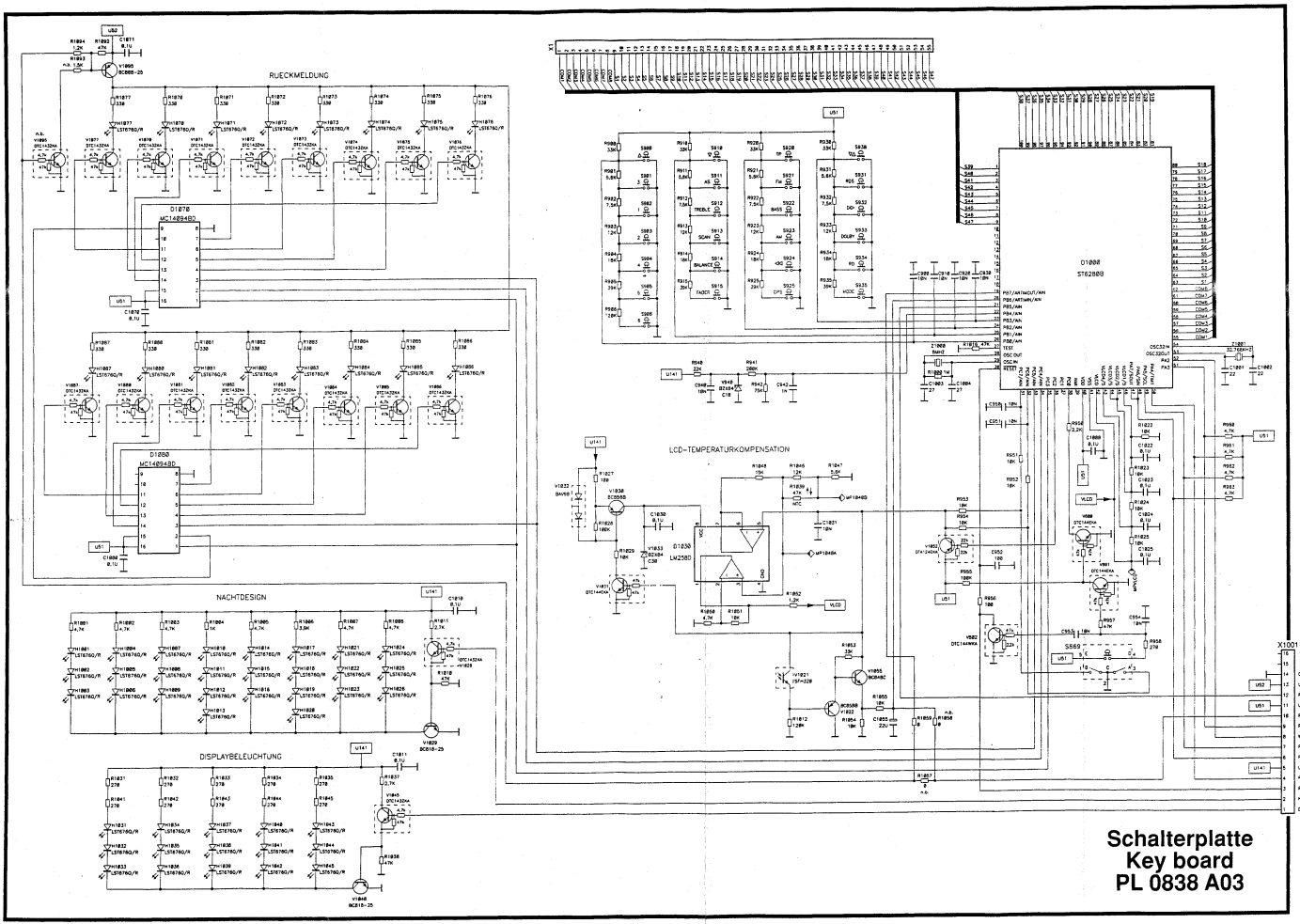
X1201 (TO X1300)
1 = CRST
2 = +5V
3 = CRQ
4 = SDA
5 = SCL
6 = +14V
7 = GND
8 = CC_SW_PLAY
9 = N.C.

X1101
1 = CAPSTAN_MOTOR –
2 = CAPSTAN_MOTOR +
3 = CAPSTAN_MOTOR
4 = LIFT_MOTOR –
5 = LIFT_MOTOR +
6 = SW_STANDBY
7 = SW_STANDBY_GND
8 = SW_PLAY_GND
9 = SW_PLAY
10 = SW_INSERT
11 = SW_INSERT_GND
12 = SW_ME / CR_GND
13 = SW_ME / CR



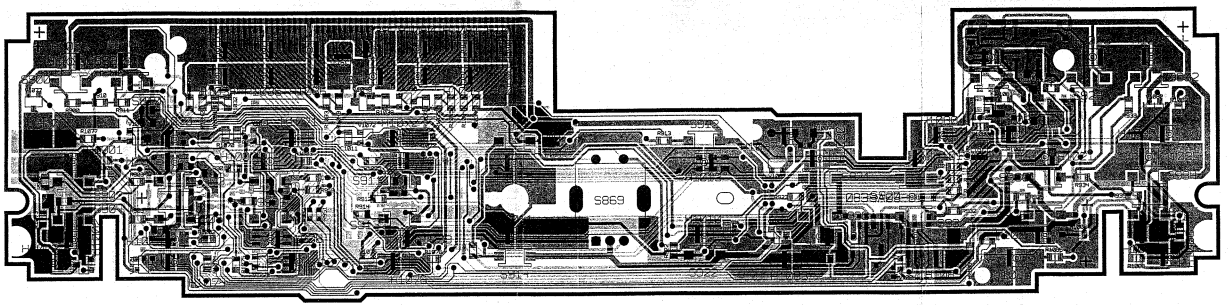
Logik-Platte
Logic board
(SCA 3.3)
Chip



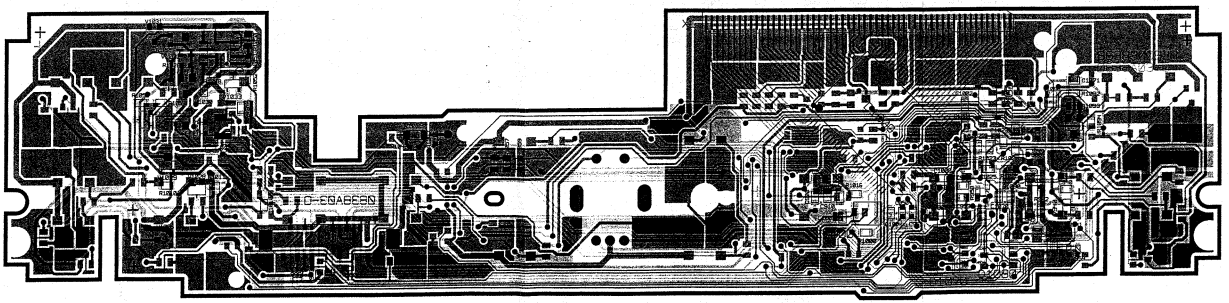


Schalterplatte
Key board
PL 0838 A03

Schalterplatte
Key board
PL 0838 A03
Chip

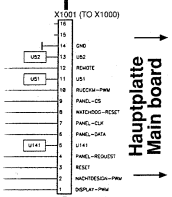


Schalterplatte
Key board
PL 0838 A03
Chip



X1001 (TO X1000)

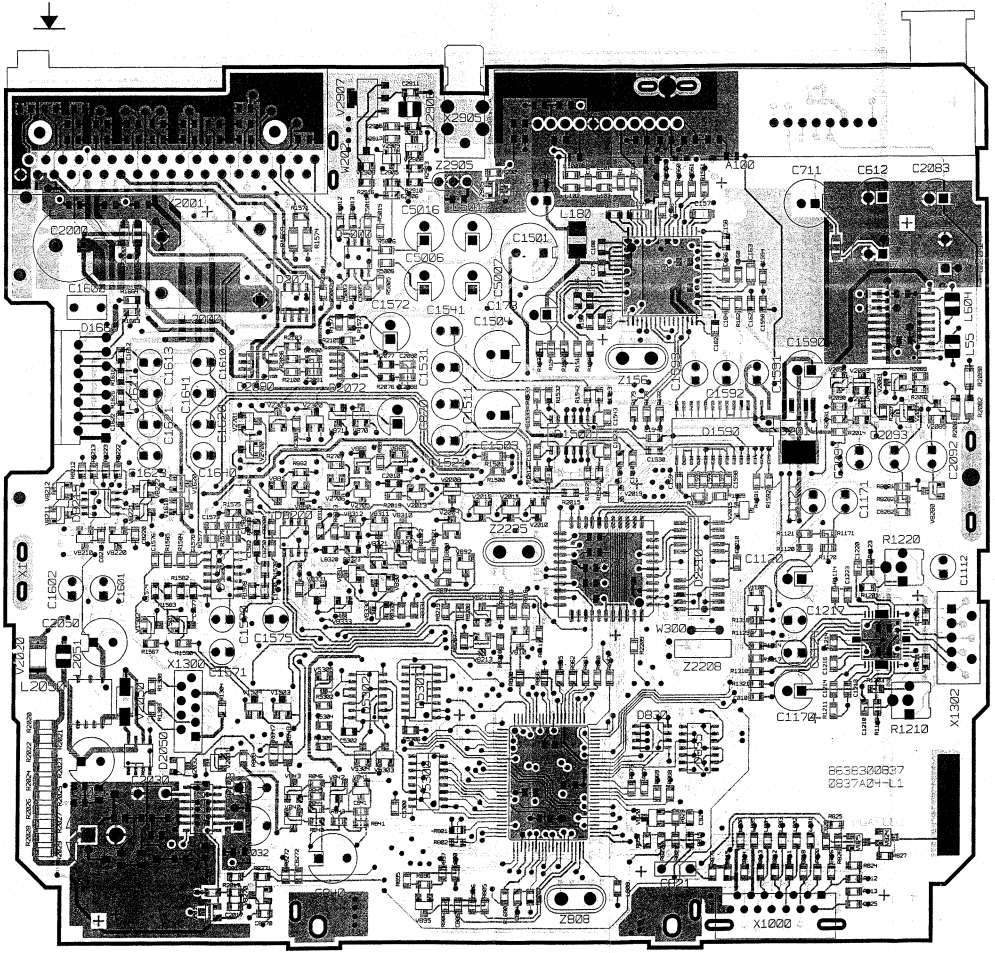
Hauptplatte
Main board



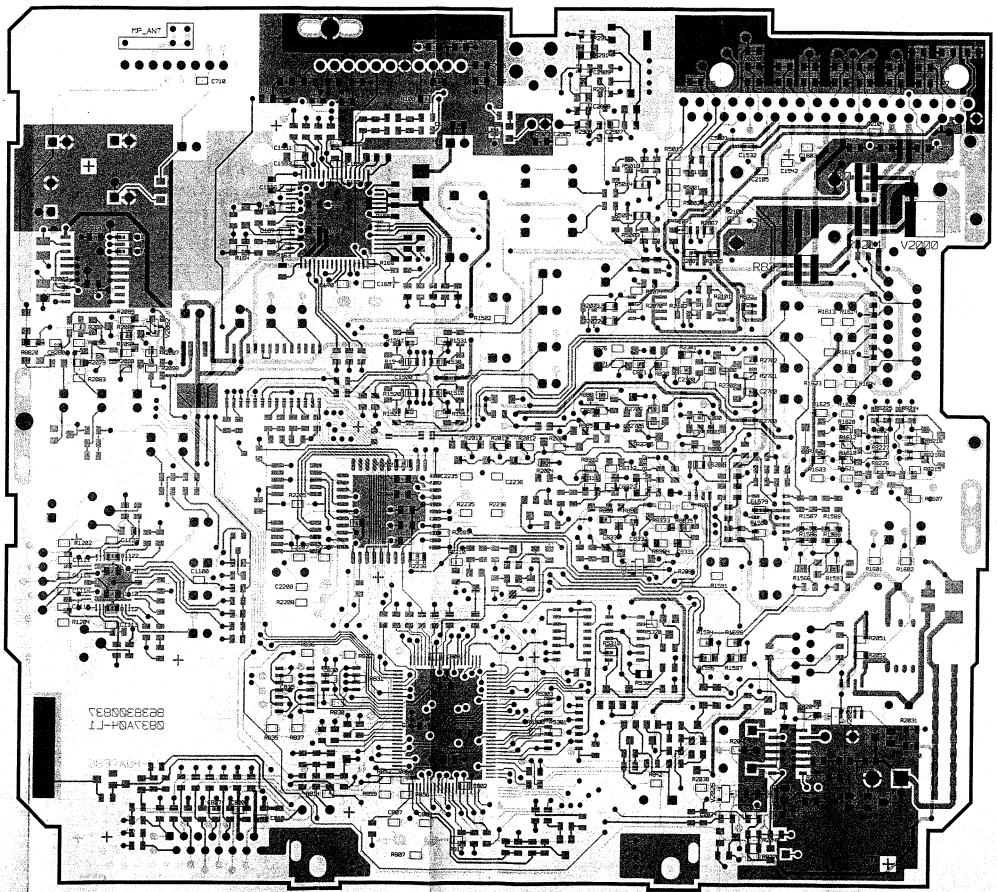
te

3

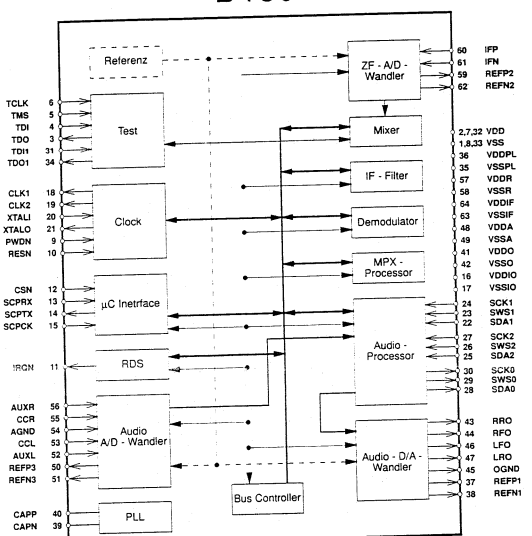
Hauptplatte
Main board
PL 0837 A04
B + Chip



Hauptplatte
Main board
PL 0837 A04
Chip

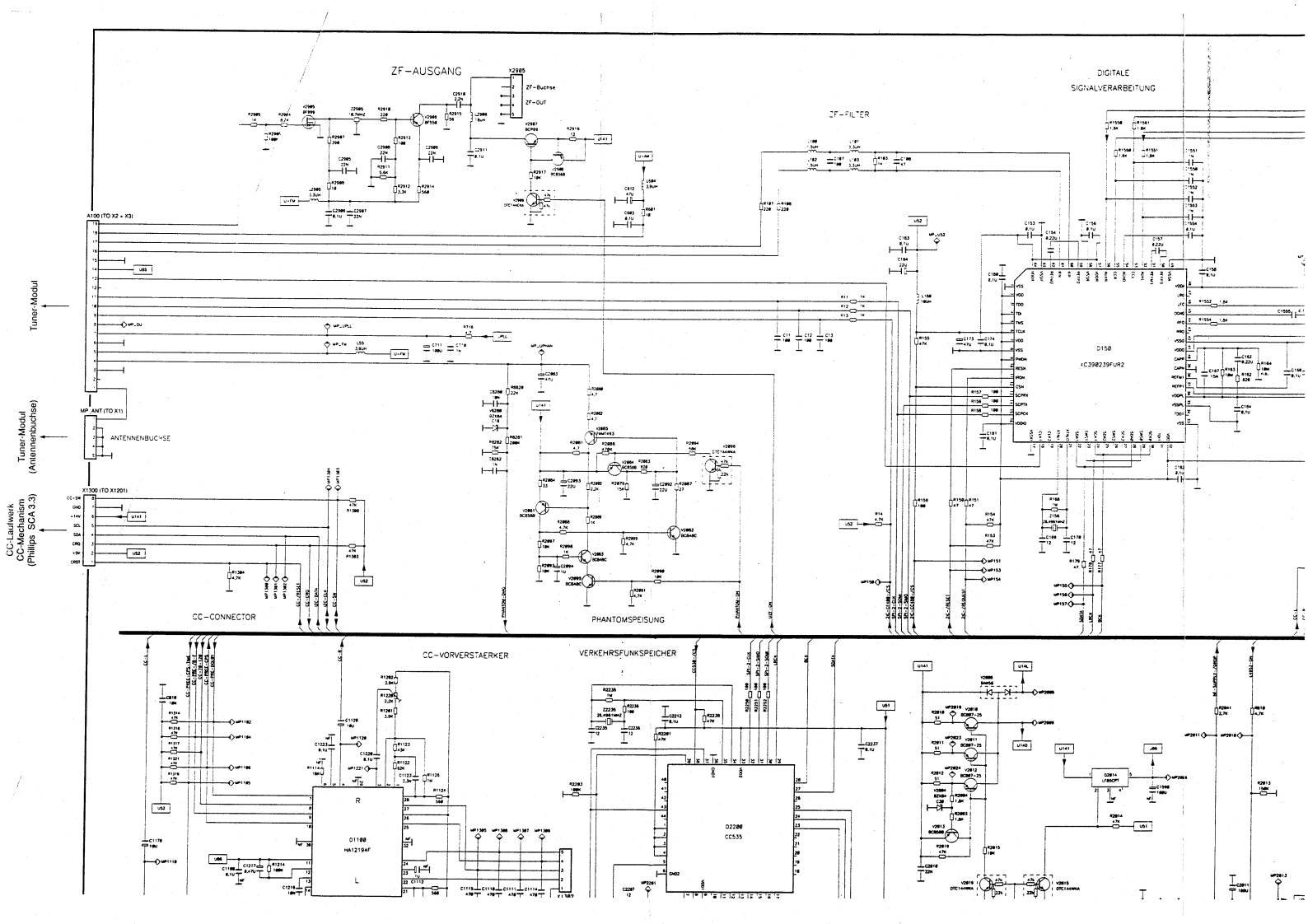


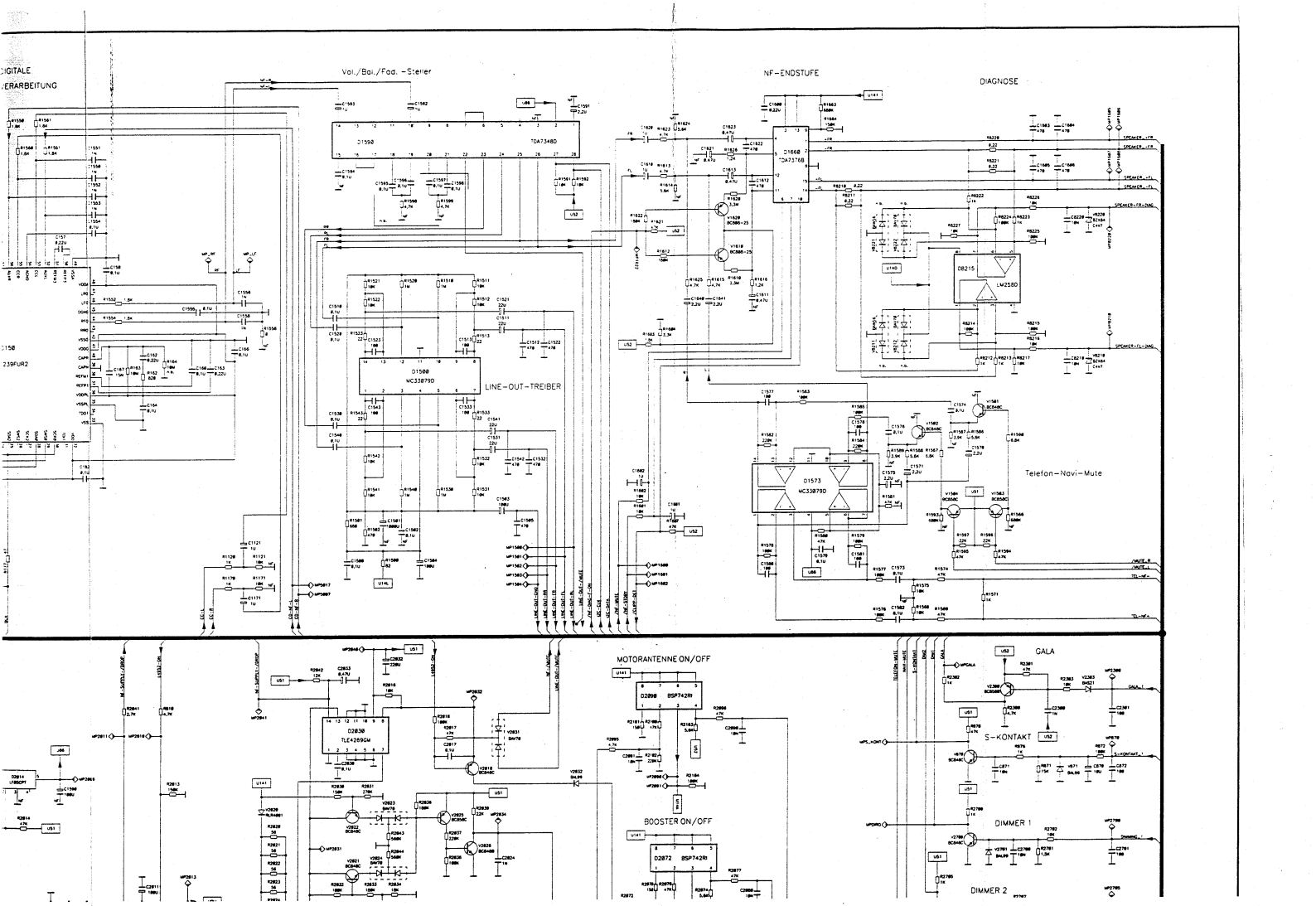
D150

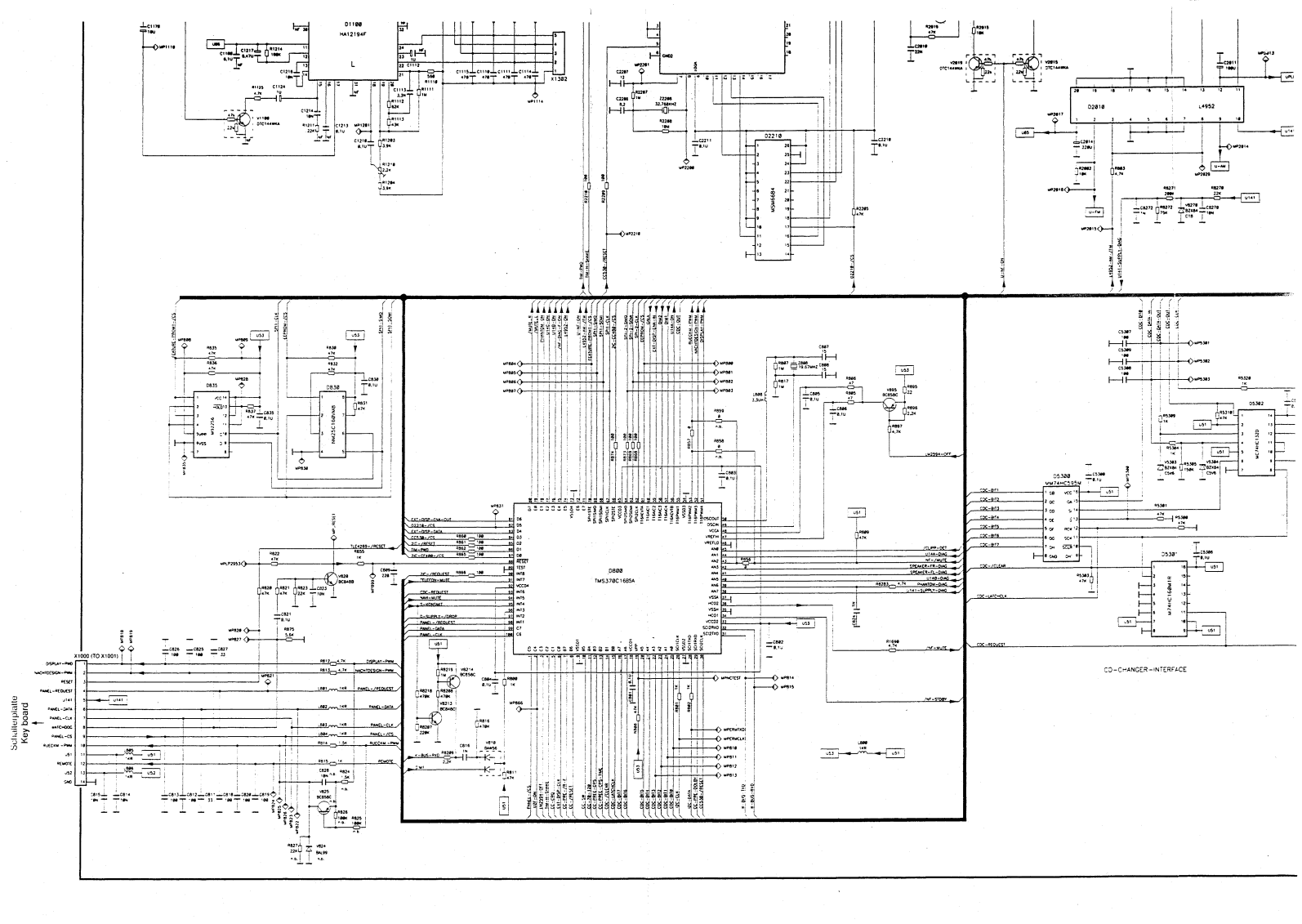


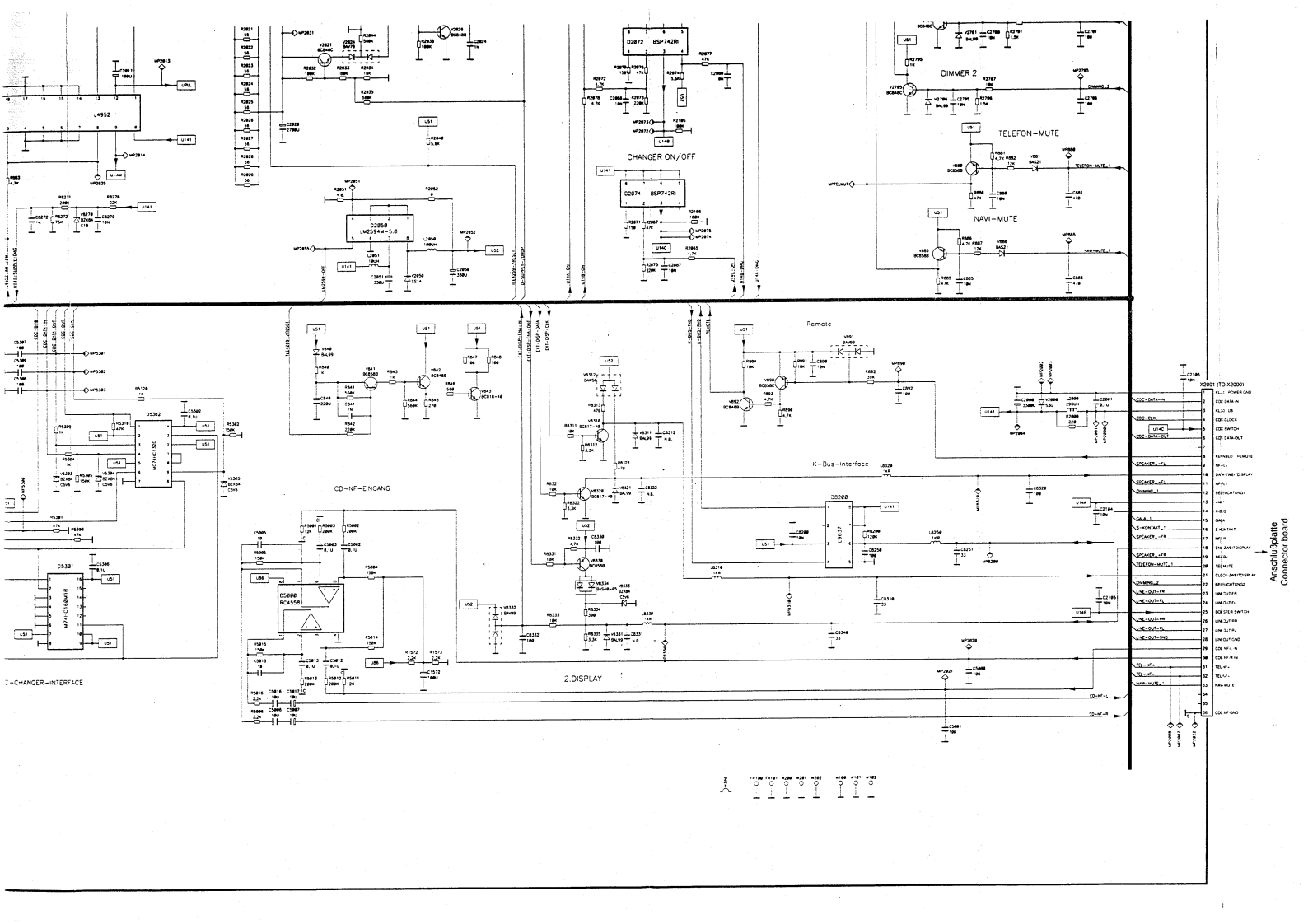
Pin-Belegung des IC D150
Digital IC D150 Pin Configuration

Pin No.	I/O	Name	Funktion	Function
1	-	VSS	Masse	Ground
2	-	VDD	5 V	5 V
4	I	TDI	Testdateneingang	Test Data Input
5	I	TMS	Test Mode	Testmode
6	I	TCKL	Test Clock	Testclock
7	-	VDD	5 V	5 V
8	-	VSS	Masse	Ground
9	-	PWDN	Power down Zustand	Power down Mode
10	I	RESN	Reset	Hardware reset (active LOW)
11	O	IRQN	RDS Alarm/SLS	RDS alarm/search stop
12	I	CSN	Chip select Eingang	Chip select µC interface
13	I	SCPRX	Serielle Daten µC Interface	Serial data µC interface IN
14	O	SCPTX	Serielle Daten µC Interface	Serial data µC interface OUT
15	I	SCPCK	Clock µC Interface	Clock µC interface
16	-	VDDIO	Plusspannung Digitale Ein-/Ausgänge	Voltage for digital I/O
17	-	VSSIO	Masse Digitale Ein-/Ausgänge	Ground for digital I/O
18	O	CKL1	Programmierbarer Clock 1	Programmable clock 1
20	I	XTALI	28,5 MHz Oszillator	Oscillator 28.5 MHz
21	O	XTALO	28,5 MHz Oszillator	Oscillator 28.5 MHz
31	I	TDI1	Testdateneingang 1	Test Input 1
32	-	VDD	5 V	5 V
33	-	VSS	Masse	Ground
35	-	VSSPLL	Masse (Minus) PLL	Ground (minus) PLL
36	-	VDDPLL	Plus PLL 5V	PLL 5V (pos.)
37	O	REFP1	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
38	O	REFN1	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
39	-	CAPN	PLL Kapazität (negativ)	PLL capacity (neg.)
40	-	CAPP	PLL Kapazität (positiv)	PLL capacity (pos.)
41	-	VDDO	Audio D/A - Wandler 5V	Audio D/A converter (+5V)
42	-	VSSO	Audio D/A - Wandler Masse	Audio D/A converter (ground)
44	O	RFO	Audio Rechts (analog)	Analogic audio right
45	-	OGND	Masse Analogausgänge	Ground
46	-	LFO	Audio Links (analog)	Analogic audio left
48	-	VDDA	5V A/D - Wandler	5V A/D - converter
49	-	VSSA	Masse A/D - Wandler	Ground A/D - converter
50	O	REFP3	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
51	O	REFN3	Audio D/A-WandlerNegative Referenz	Audio D/A converter (neg. reference)
52	I	AUXL	Externer Eingang links	Auxillary left
53	I	CCL	Cassette Eingang links	Cassette input left
54	-	AGND	Audioeingänge Masse	Ground for Audio inputs
55	I	CCR	Cassette Eingang rechts	Cassette input right
56	I	AUXR	Externer Eingang rechts	Auxillary left right
57	-	VDDR	5 V	5 V
58	-	VSSR	Masse	Ground
59	O	REFP2	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
60	I	IFP	ZF Eingang (plus)	Positif IF input
61	I	IFN	ZF Eingang (minus)	IF input (neg.)
62	O	REFN2	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
63	-	VSSIF	ZF A/D - Wandler (minus)	IF A/D converter (-)
64	-	VDDIF	ZF A/D - Wandler 5 V	IF A/D converter (+5V)

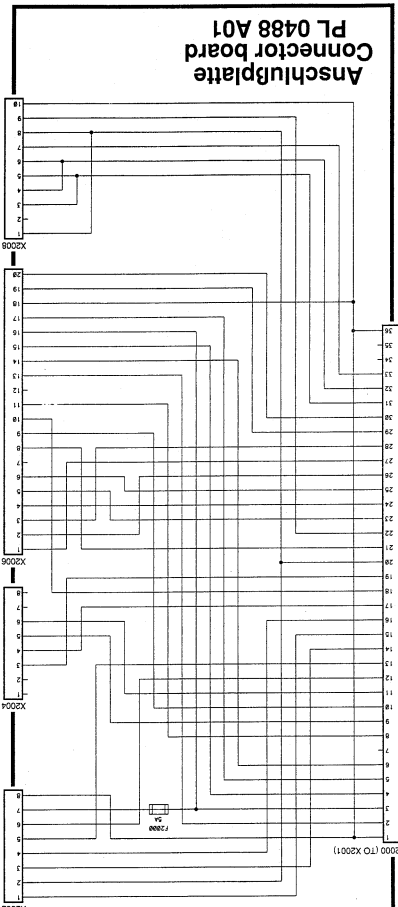

ZF-AUSGANG
ZF-Buchse
ZF-out
ZF-FILTER
DIGITALE
SIGNALVERARBEITUNG
A100 (TO X2 + X3)
Tuner-Modul
MP_ANT (TO X1)
ANTENNENBUCHSE
Tuner-Modul
(Antennenbuchse)
X1300 (TO X1201)
CC-Laufwerk
CC-Mechanism
(Phillips SCA 3.3)
CC-CONNECTOR
CC-VORVERSTAERKER
VERKEHRSFUNKSPEICHER
PHANTOMSPEISUNG
D150
XC39B239FUR2
D1188
HA12194F
D2200
CC535


IGITALE
ERARBEITUNG
Vol./Bal./Fad. -Steller
NF-ENDSTUFE
DIAGNOSE
LINE-OUT-TREIBER
Telefon-Navi-Mute
MOTORANTENNE ON/OFF
BOOSTER ON/OFF
GALA
S-KONTAKT
DIMMER 1
DIMMER 2


D1100
HA12194F
D2210
M34466M4
D2810
L4952
D835
D838
D800
TMS370C1685A
D5300
MM74HC533M
D5302
D5303
CD-CHANGER-INTERFACE
Schalterplatte
Key board


L4952
D2872 BSP742R
CHANGER ON/OFF
D2874 BSP742R
D2850 LM2594M-5.0
DIMMER 2
TELEFON-MUTE
NAVI-MUTE
Remote
CD-NF-EINGANG
K-Bus-Interface
2.DISPLAY
C-CHANGER-INTERFACE
D5302
D5300
D5000 RC4558
D8200
X2001 (TO X2000)
Anschlußplatte
Connector board

Anschlußplatte
Connector board
PL 0488 A01



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